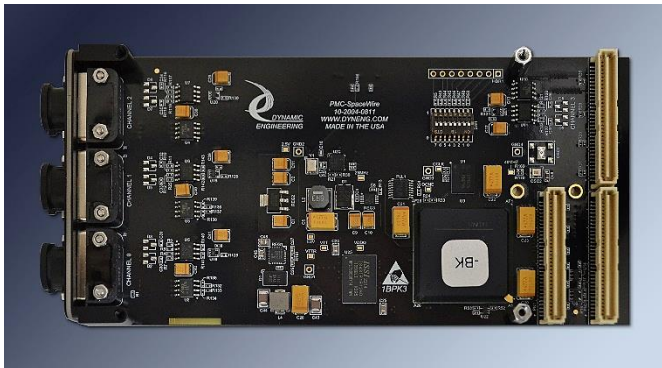
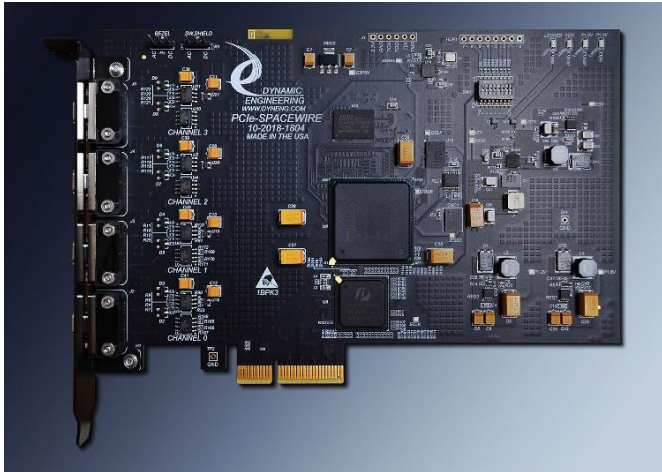




SpaceWire

Advances and Enhancements

<https://www.dyneng.com/SpaceWire.html>



- Designed for rapid user integration with included reference software
- More than 32 Mbytes per port (default), 256 Mbytes of user allocatable memory
- 8 Independent scatter gather DMA engines to support the SpaceWire ports
- Fast: Rated 1-200 MHz. Autobauding on Rx ports. Tx ports have programmable PLL reference plus divider
- Low-latency, FIFO based design
- PCIe Version has additional enhancement with 50% more bandwidth on the host interface
- Easy-to-use: hierarchical design with a direct memory map
- Available native in PCIe, PMC, PCI, PC104p, and ccPMC, adaptable to cPCI

**NEW
PRODUCT**

SpaceWire-MONITOR: Traffic between ports 0 and 1 is monitored, decoded and stored in parallel. The application uses DMA to transfer the two ports received data to host memory. The data is time tagged. Windows and Linux support.

Popular add-ons include SpaceWire Cables and test equipment, pick the DESWBO for debugging with LEDs and test points

