



DYNAMIC ENGINEERING

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Est. 1988

Statement of Volatility

PN: ccPMC-HOTlink

Manufacturer Part Number: PMC-ccHOTlink, includes all types (-PA1, etc.)

Manufacturer Part Description: PMC with FPGA, FIFO options, RS485 and ECL, rear IO, PLL

Memory Type: FLASH, SRAM

Memory Size: varies with installed FPGA

Volatility: SRAM cleared by power cycle. FLASH is retained through power cycle. FLASH is not accessible by user for writing or read-back. FLASH is used to reload FPGA at power-on.

User Accessible: SRAM within FPGA is configured into registers etc. FPGA features are R/W [in many cases] to user software. SRAM contents are lost at power down and re-written with base design from FLASH upon power-on.

Clearing Procedure: Power cycle or re-write data stored contents per approved procedure. SRAM model for internal.

Notes or Warnings: None